For C			ECIFICAT	FOR SPECIFICATION		
Custo	mer Model I	No	: APPROVAL	FOR SAMPLE		
Modu	ıle No.:	TIAN-T070Q01-26	Date : 2	2019-03-26		
e of Con						
No.		Item		Page		
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Appro	ved By		Comment			
PREP	ARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DE		

2. Revision Record

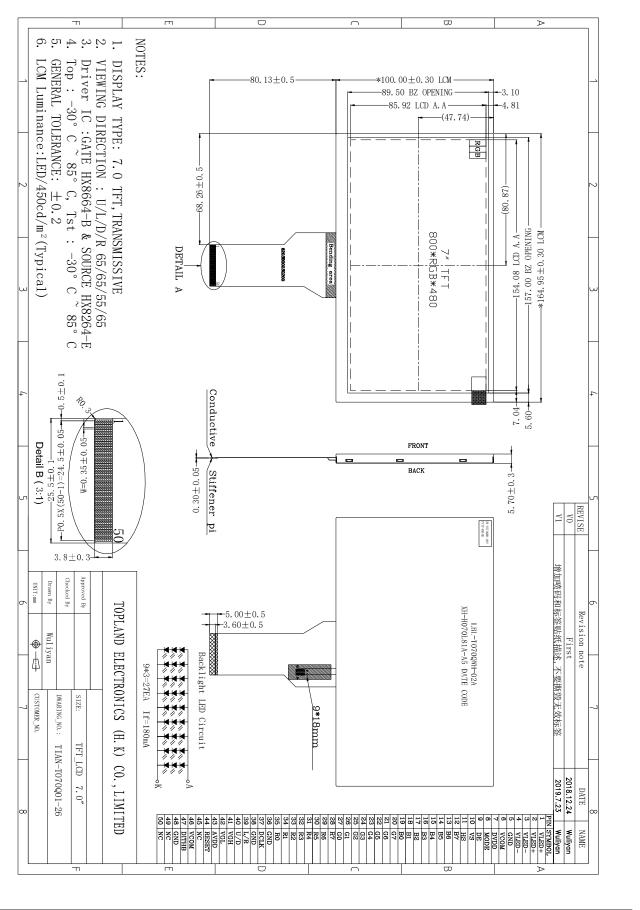
Date	Rev.No.	Page	F	Revision Item	S	Prepared
2019-01-07	V0		First			GZH
2019-03-26	V1		Updated conditions	Typical	operation	GZH

3. General Specifications

TIAN-T070Q01-26 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 7.0" display area contains 800x480 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	12	O'Clock	
Gray scale inversion direction	6	O'Clock	
Operating temperature	-30~+85	°C	
Storage temperature	-30~+85	°C	
Module size	Refer to outline drawing	mm	
Active Area(W×H)	154.08X85.92	mm	
Number of Dots	800×480	dots	
Controller	HX8664-B & HX8264-E	-	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	3X9-LEDs (white)	pcs	
Weight		g	
Interface	RGB888	-	

4. Outline. Drawing



5. Absolute Maximum Ratings(Ta=25 °C)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V,Ta=25 °C)

Item	Symbol	Min.	Max.	Unit	Note
	V _{dvdd}	-0.5	5.0	V	1, 2
	Vavdd	6.5	13.5	V	
Power Supply Voltage	V_{GH}	-0.5	42.0	V	
	Vgl	-20	0.3	V	
	V _{GH} -V _{GL}	-	40.0	V	

Notes:

- 1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
- 2. $V_{DVDD} > V_{SS}$ must be maintained.
- 3. Please be sure users are grounded when handing LCD Module.

5.2 Typical operation conditions

			Values			
ltem	Symbol	Min.	Тур.	Max.	Unit	Remark
	DVDD	3.0	3.3	3.6	V	
Dowerveltege	AVDD	10.2	10.4	10.6	V	
Power voltage	VGH	14.5	15	15.5	V	
	VGL	-10.5	-10.0	-9.5	V	
Input signal voltage	VCOM	3.54	4.04	4.54	V	
Input logic high voltage	VIH	0.7 DVDD	_	DVDD	V	
Input logic low voltage	VIL	0	-	0.3DVDD	V	

5.3 Environmental Absolute Maximum Ratings.

Item	Stor	age	Operat	ting	Note
item	MIN.	MAX.	MIN.	MAX.	NOLE
Ambient Temperature	-30 °C	85 ℃	-30 °C	85 ℃	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40 °С:85%RH МАХ.

Ta>=40 $^{\circ}C$:Absolute humidity must be lower than the humidity of 85%RH at 40 $^{\circ}C$.

6. Electrical Specifications and Instruction Code

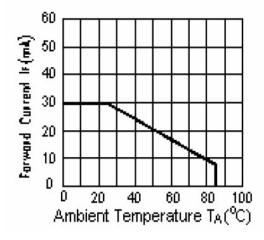
Parame	Parameter Symbol		Condition	Min	Тур	Max	Unit	Note
Power su	Power supply		Ta=25° ℃	3.0	3.3	3.6	V	
Input	'H'	Vih	V _{DVDD} =3.3V	0.7V _{DVDD}	-	V _{DVDD}	V	
voltage	'L'	VIL	V _{DVDD} =3.3V	0	-	0.3Vdvdd	V	
Curren	Current		Normal mode	-	45	60	mA	
Consump	otion	Idvdd2	Sleep mode	-	0.05	0.1	mA	

6.1 Electrical characteristics(Vss=0V,Ta=25 °C)

Note: 1. Tested in 1×1 chessboard pattern.

6.2 LED backlight specification(VSS=0V ,Ta=25°C)

Item	Symbol	Condition	Min	Тур	Max	Unit	Note
Supply voltage	Vf	lf=9X20mA	-	9.0	-	V	
Uniformity	∆Вр	lf=9X20mA	75	-	-	%	



ILED VS TEMP

6.3 Interface signals

Pin No.	Symbol	I/O	Function			
1-2	VLED+	Р	LED back light(Anode)			
3-4	VLED-	Р	LED back light(Cathode)			
5	GND	Р	Ground.			
6	VCOM	Р	Common voltage			
7	DVDD	Р	Power supply			
8	MODE	I	DE/YSNC mode select			
9	DE		Data enable pin			
10	VS	I	Frame sync signal			
11	HS	1	Line sync signal			
12-19	B7~B0	I	Blue data bus			
20-27	G7~G0	I	Green data bus			
28-35	R7~R0	I	Red data bus			
36	GND		Ground.			
37	DCLK	I	Data clock			
38	GND	Р	Ground.			
39	L/R		Right/Left sequence control of source driver			
40	U/D		Gate driver Up/Down scan control of gate driver			
41	VGH	I	Gate on voltage			
42	VGL	Р	Gate off voltage			
43	AVDD	Р	power for analog circuit			
44	RESET	1	Chip reset pin			
45	NC	-	No connection.			
46	VCOM	Р	Common voltage			
47	DITHB	I	Dithering function enable control.Normally pull high. DITHB=1, enable disable internal dithering function. DITHB =0,disable internal dithering function.			
48	GND	Р	Ground			
49-50	NC	-	No connection.			

6.4 AC characteristics

Descentes	Ormahad	1012121	Spec.	100222	11
Parameter	Symbol	Min.	Тур.	Max.	Unit
HS setup time	Thst	8		-	ns
HS hold time	Thhd	8		-	ns
VS setup time	Tvst	8	-	-	ns
VS hold time	Tvhd	8		-	ns
Data setup time	Tdsu	8	19 - 21	-	ns
Data hold time	Tdhd	8	- 1 - 1	-	ns
DE setup time	Tesu	8		-	ns
DE hold time	Tehd	8	-	-	ns
VDD Power On Slew rate	TPOR	-	-	20	ms
RSTB pulse width	TRst	10	-	-	μs
CLKIN cycle time	Tcph	20	-	-	ns
CLKIN pulse duty	Tcwh	40	50	60	%
Output stable time	Tsst		-	6	μs

Horizontal Timing

Parameter	Sumbol		Spec.		Unit
Farameter	Symbol	Min.	Тур.	Max.	Unit
Horizontal Display Area	thd	-	800	-	DCLK
DCLK frequency	fclk		33.3	50	MHz
One Horizontal Line	th	862	1056	1200	DCLK
HS pulse width (Min.)	thpw	1			DCLK
HS pulse width (Typical.)	thpw		-		DCLK
HS pulse width (Max.)	thpw		40	×	DCLK
HS Back Porch (Blanking)	thb	46	46	46	DCLK
HS Front Porch	thfp	16	210	354	DCLK
DE mode Blanking	th-thd	45	256	400	DCLK

Vertical Timing

Parameter	Symbol		Unit		
Farameter	Symbol	Min.	Тур.	Max.	Unit
Vertical Display Area	tvd		480		TH
VS period time	tv	510	525	650	TH
VS pulse width	tvpw	1	-	20	TH
VS Back Porch (Blanking)	tvb	23	23	23	TH
VS Front Porch	tvfp	7	22	147	TH
DE mode Blanking	tv-tvd	4	45	170	TH

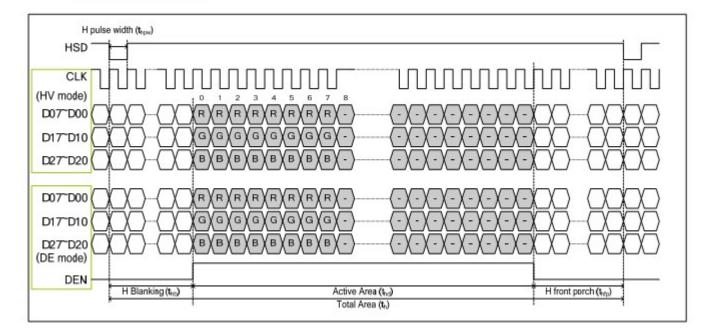
•parallel 24-bit RGB mode

Desemator	Sumbal	Spec.			Unit	Conditions	
Parameter	Parameter Symbol Min. Typ		Тур.	Max.	Unit	Conditions	
CLKIN Frequency	Fclk	-	40	50	MHz	VDD = 3.0V ~ 3.6V	
CLKIN Cycle Time	Tclk	20	25	-	ns	-	
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk	
Time from HSD to Source Output	Thso	-	20	-	CLKIN	-	
Time from HSD to LD	Thld	-	20	-	CLKIN	1.5	
Time from HSD to STV	Thstv	-	2	-	CLKIN	(*)	
Time from HSD to CKV	Thckv	-	20	-	CLKIN	-	
Time from HSD to OEV	Thoev	-	4	-	CLKIN	-	
LD Pulse Width	Twld	-	10	-	CLKIN	-	
CKV Pulse Width	Twckv	-	66	-	CLKIN	-	
OEV Pulse Width	Twoev	-	74	-	CLKIN	-	

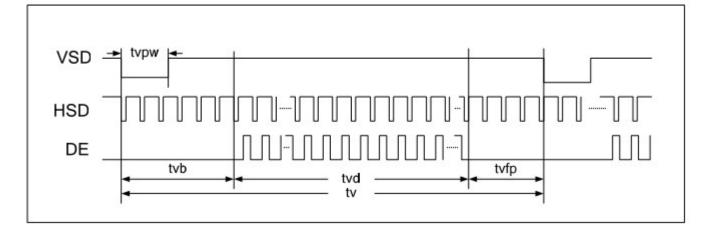
Page:10/19

6.5 Data input format

Horizontal timing

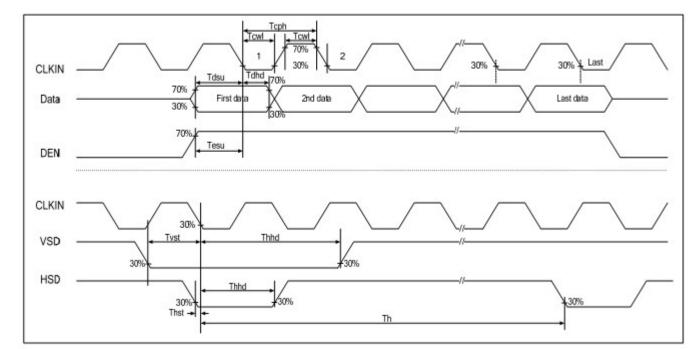


Vertical Timing

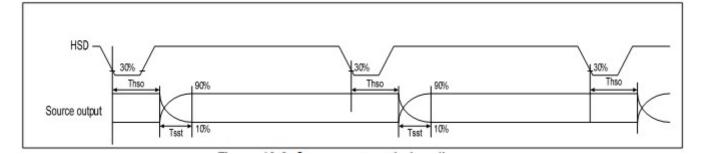


6.6 Timing diagram

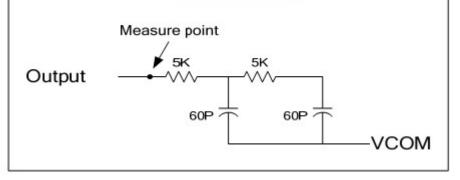
6.6.1 clock input and data timing waveform



6.6.2 Source output timing waveform



Output load condition



7. Optical Characteristics

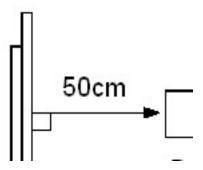
Item	Symbol		Condition	Min.	Тур.	Max.	Unit	Note
Brightness	Bp ⊿Bp		<i>θ</i> =0° Φ=0°	-	450	-	Cd/m ²	1
Uniformity				75	-	-	%	1,2
Viewing Angle	3:00		- Cr≥10	-	65	-	- Deg	3
	6:00			-	55	-		
	9:00			-	65	-		
	12	2:00		-	65	-]	
Contrast Ratio		Cr	<i>θ</i> =0°	400	500	-	-	4
Response	Tr Tf		Φ=0°	-	25	50	ms	- 5
Time				-	25		ms	
	W	х	<i>θ</i> =0° Φ=0°		0.33		-	
Color of CIE Coordinate	vv	у			0.35		-	
	R -	х			0.51		-	
		у			0.34			1,6
	G —	х			0.31			
		У			0.56			
	B x	х			0.15		-	
		У			0.14		-	
NTSC Ratio	S			-	50	-	%	

Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment BM-7 (Φ5mm) Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25 ℃.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

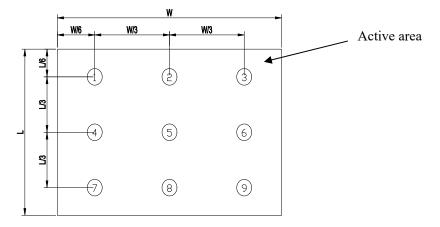


Note 2: The luminance uniformity is calculated by using following formula.

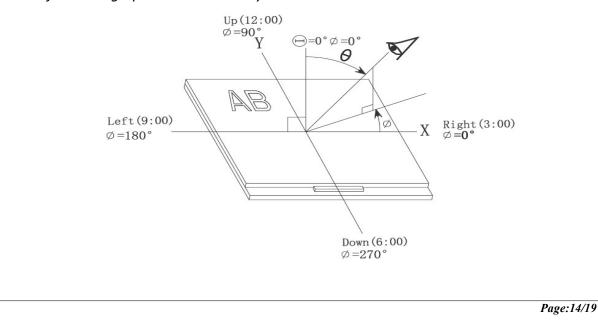
∠Bp = Bp (Min.) / Bp (Max.)×100 (%)

Bp (Max.) = Maximum brightness in 9 measured spots

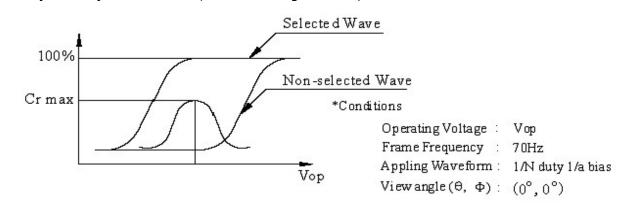
Bp (Min.) = Minimum brightness in 9 measured spots.



Note 3: The definition of viewing angle: Refer to the graph below marked by ϑ and Φ



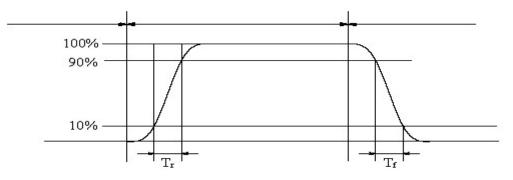
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



 $Contrast \ ratio(Cr) = \frac{Brightness \ of \ selected \ dots}{Brightness \ of \ non-selected \ dots}$

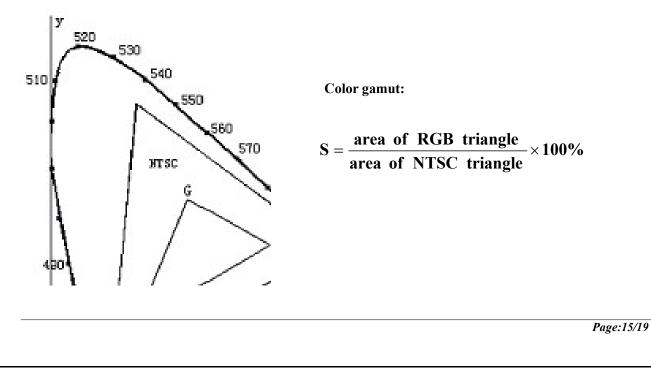
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



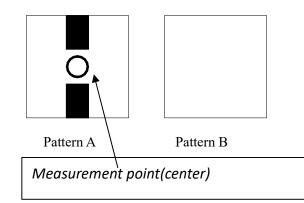
The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.



Note 7: Definition of cross talk.

Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness//pattern A Brightness*100



Electric volume value=3F+/-3Hex

8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion		
1	High Temperature Storage	85℃±2℃ 96H Restore 2H at 25℃ Power off			
2	Low Temperature Storage	-30℃±2℃ 96H Restore 2H at 25℃ Power off	 1. After testing, cosmetic and electrical defects should not 		
3	High Temperature Operation	85°C±2°C 96H Restore 2H at 25°C Power on			
4	Low Temperature Operation	-30℃±2℃ 96H Restore 4H at 25℃ Power on	happen.2. Total currentconsumption shouldnot be more than twice		
5	High Temperature/Humidity Operation	60℃±2℃ 90%RH 96H Power on	of initial value.		
6	Temperature Cycle	-30°C			
7	Vibration Test	10Hz~150Hz, 100m/s², 120min	Not allowed cosmetic		
8	Shock Test	Half- sine wave,300m/s²,11ms	and electrical defects.		

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9. Precautions for Use of LCD Modules

9.1 Handling Precautions

- 9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol — Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water - Ketone - Aromatic solvents

9.1.6 Do not attempt to disassemble the LCD Module.

- 9.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - *c.* To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.2 Storage precautions

- 9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0 $^{\circ}$ C \sim 40 $^{\circ}$ C

Relatively humidity: ≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.3 The LCD modules should be no falling and violent shocking during transportation, and also should

avoid excessive press, water, damp and sunshine.

<u>END</u>